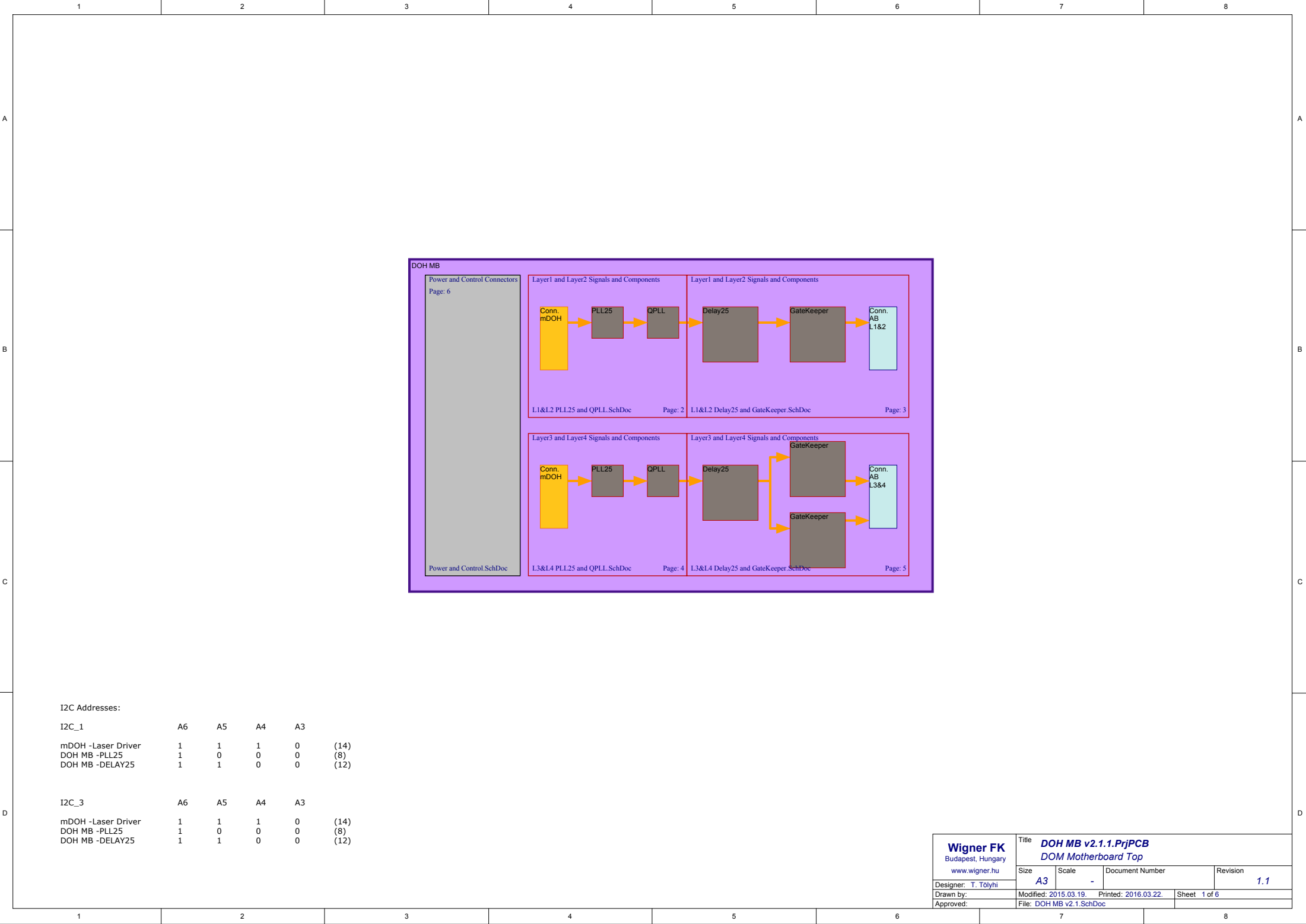




I2C Addresses:

I2C_1	A6	A5	A4	A3	
mDOH -Laser Driver	1	1	1	0	(14)
DOH MB -PLL25	1	0	0	0	(8)
DOH MB -DELAY25	1	1	0	0	(12)

I2C_3	A6	A5	A4	A3	
mDOH -Laser Driver	1	1	1	0	(14)
DOH MB -PLL25	1	0	0	0	(8)
DOH MB -DELAY25	1	1	0	0	(12)

Wigner FK
Budapest, Hungary
www.wigner.hu

Designer: T. Tolyhi

Drawn by:

Approved:

Title
DOH MB v2.1.1.PrjPCB
DOM Motherboard Top

Size
A3

Scale
-

Document Number

Revision
1.1

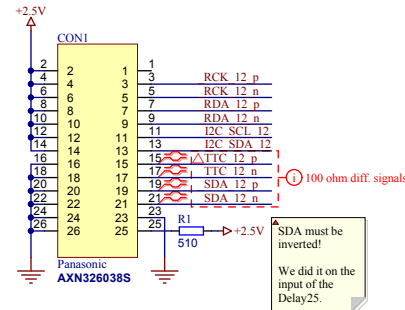
Modified: 2015.03.19.

Printed: 2016.03.22.

Sheet 1 of 6

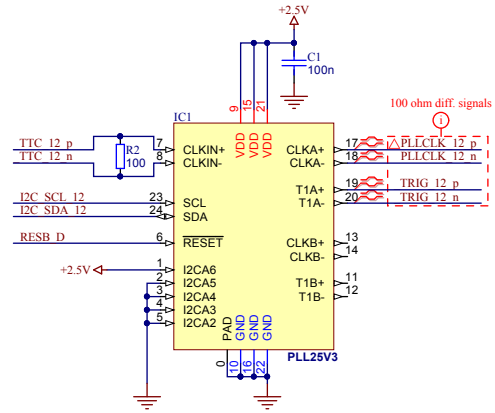
File: DOH MB v2.1.SchDoc

mDOH Connector

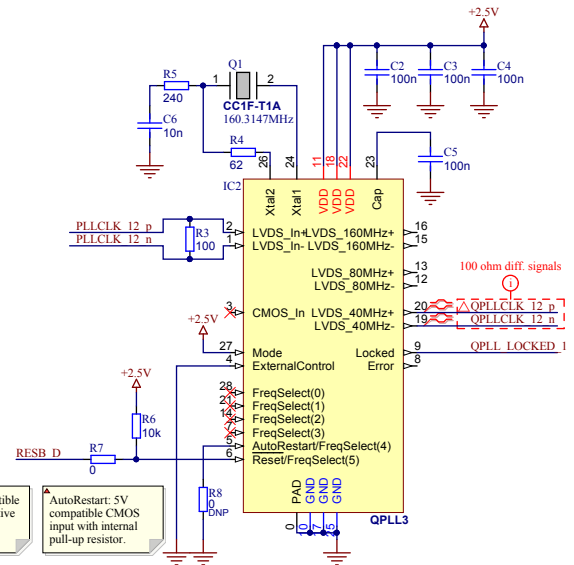


TPLL

I2C_Address: 1000 (8)



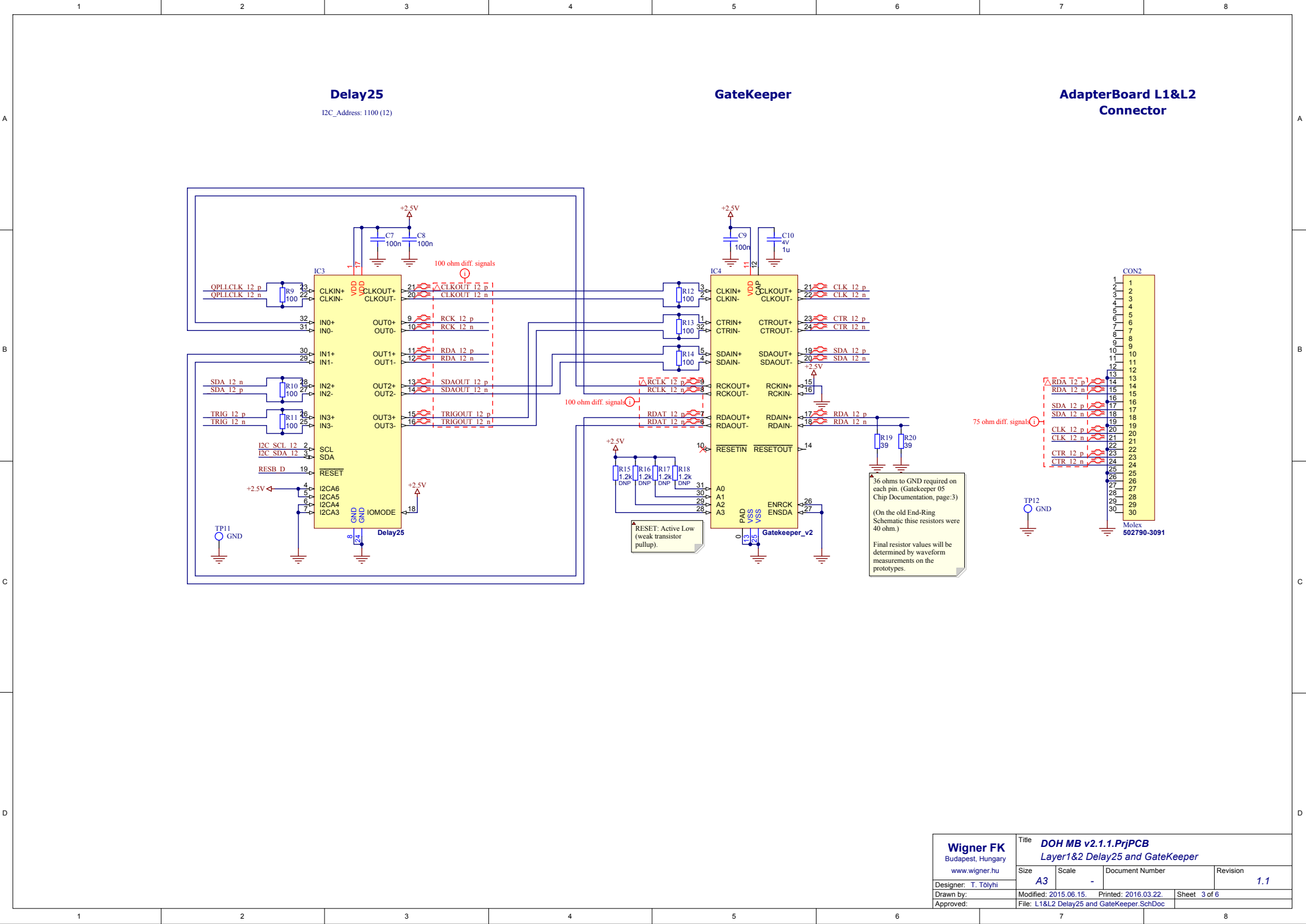
QPLL



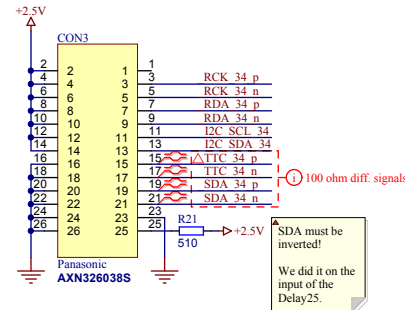
Reset: 5V compatible CMOS input. Active low reset signal.

AutoRestart: 5V compatible CMOS input with internal pull-up resistor.

QPLL Configuration:
ExternalControl = "0" and AutoRestart = "0":
Automatic restart of the PLL is disabled. A frequency calibration cycle will only occur after a reset.
ExternalControl = "0" and AutoRestart = "1":
Automatic restart is enabled. A frequency calibration cycle will occur each time the PLL is detected to be unlocked or after a reset.

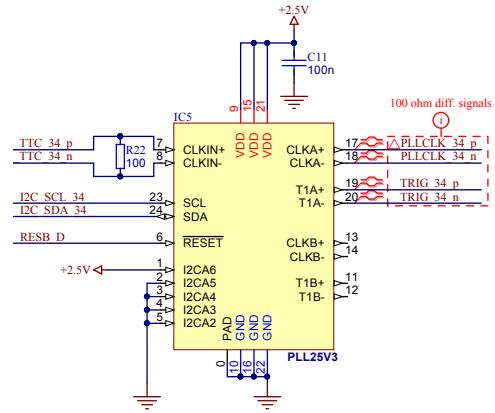


mDOH Connector

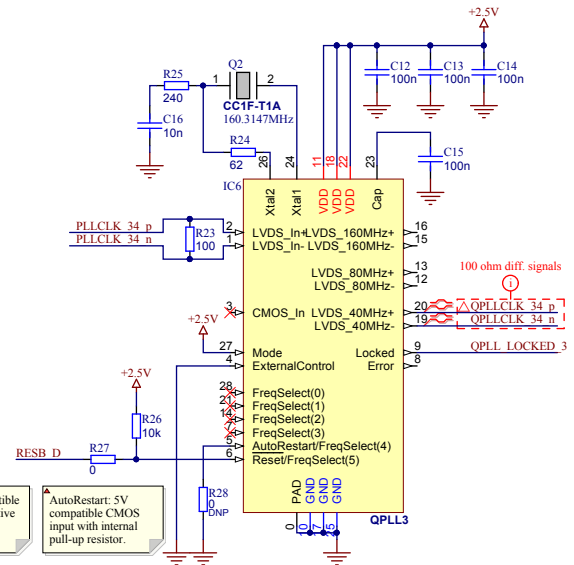


TPLL

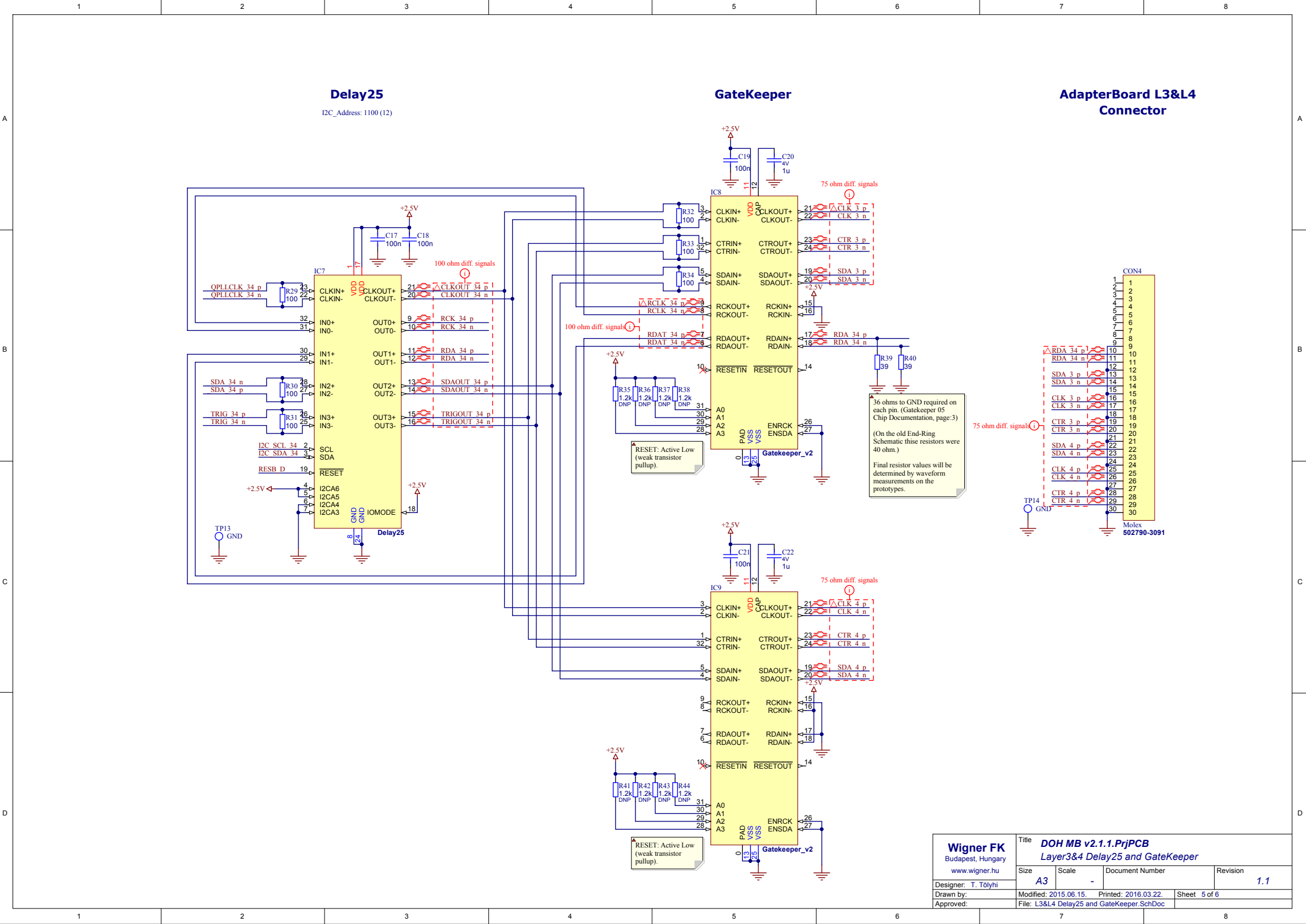
I2C_Address: 1000 (8)



QPLL



QPLL Configuration:
ExternalControl = "0" and AutoRestart = "0":
Automatic restart of the PLL is disabled. A frequency calibration cycle will only occur after a reset.
ExternalControl = "0" and AutoRestart = "1":
Automatic restart is enabled. A frequency calibration cycle will occur each time the PLL is detected to be unlocked or after a reset.



Wigner FK Budapest, Hungary www.wigner.hu	Title <i>DOH MB v2.1.1.PrjPCB</i> <i>Layer3&4 Delay25 and GateKeeper</i>			
	Size A3	Scale -	Document Number	Revision 1.1
Designer: T. Tolyhi	Modified: 2015.06.15.		Printed: 2016.03.22.	Sheet 5 of 6
Drawn by:	File: L3&L4 Delay25 and GateKeeper.SchDoc			
Approved:				

